

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09992076
	Filing Date		2001-11-13
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01210M	

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/JP/	1	6002398	B1	1999-12-14	Wilson, Daniel	Entire Document
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/JP/	5	6121965	B1	2000-09-19	Kenney, et al.	Entire Document
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/JP/	9	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
/JP/	10	6438565	B1	2002-08-20	Ammirato, et al.	Entire Document
/JP/	11	6456304	B1	2002-09-24	Angiulo, et al.	Entire Document
/JP/	12	6592626	B1	2003-07-15	Bauchot, et al.	Entire Document
/JP/	13	6613098	B1	2003-09-02	Sorge, et al.	Entire Document
/JP/	14	6701508	B1	2004-03-02	Bartz, et al	Entire Document
/JP/	15	6711731	B1	2004-03-23	Weiss, Mitchell	Entire Document
/JP/	16	6715132	B1	2004-03-30	Bartz, et al.	Entire Document
/JP/	17	6825869	B1	2004-11-30	Bang Cnyder, Werner	Entire Document
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/JP/	1	20020122060	A1	2002-09-05	Markel	Entire Document
/JP/	2	20030229877	A1	2003-12-11	Bersch, et al.	Entire Document
/JP/	3	20040221238	A1	2004-11-04	Cifra, et al.	Entire Document
/JP/	4	20020116168	A1	2002-08-22	Kim, Mahn Bok	Entire Document
/JP/	5	20020156885	A1	2002-10-24	Thakkar, Bina Kunal	Entire Document
/JP/	6	20020156998	A1	2002-10-24	Casselman, Steven	Entire Document
/JP/	7	20020091739	A1	2002-07-11	Ferlitsch et al.	Entire Document
/JP/	8	20020156929	A1	2002-10-24	Hekmatpour, Amir	Entire Document
/JP/	9	20020188910	A1	0220-12-12	Zizzo, Claudio	Entire Document

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FOREIGN PATENT DOCUMENTS

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/JP/	9	USPTO Final Rejection for 10/008,096, dated 09/04/2007	<input type="checkbox"/>
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/JP/	16	USPTO Non-Final Rejection for 10/008,096, dated 06/14/2004	<input type="checkbox"/>
/JP/	17	ANONYMOUS, "F/Port: Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm pages 1-25	<input type="checkbox"/>
/JP/	18	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm 3/3/2005	<input type="checkbox"/>

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/JP/	19	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000)	<input type="checkbox"/>
/JP/	20	"Webster's Third New International Dictionary", 1996, G. & C. Merriam Company pages 1328-1329	<input type="checkbox"/>
/JP/	21	Five unnumbered pages from the Provisional Application 60/243708 10/26/2000	<input type="checkbox"/>
/JP/	22	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM: "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996, pages 315-318	<input type="checkbox"/>
/JP/	23	HINTZ ET AL.; "MICROCONTROLLERS", 1992, McGraw-Hill, pages 29-37	<input type="checkbox"/>
/JP/	24	ITO, SERGIO AKIRA AND CARRO, LUIGI, "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000, pages 397-402	<input type="checkbox"/>
/JP/	25	Julio Faura et al.: "A NOVEL MIXED SIGNAL PROGRAMMABLE DEVICE WITH ON-CHIP MICROPROCESSOR", 1997, IEEE 1997 Custom Integrated Circuits Conference, pages 103-568	<input type="checkbox"/>
/JP/	26	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3, pages 565-568	<input type="checkbox"/>
/JP/	27	ROBINSON, GORDON D: "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes, pages 749-754	<input type="checkbox"/>
/JP/	28	WALTERS, STEPHEN: "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2, pages 4-10	<input type="checkbox"/>
/JP/	29	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001, pp. 1-25, cited by examiner	<input type="checkbox"/>

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/JP/	30	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/2001021905250/http://cypress.micro.com/~tl...>, Feb. 19, 2001, pp. 1-21, cited by examiner	<input type="checkbox"/>
/JP/	31	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000, pp. 346-349	<input type="checkbox"/>
/JP/	32	Hsieh et al., "Modeling Micro-Controller Peripherals for High-Level Co-Simulation and Synthesis," IEEE, 1997, pp. 127-130...	<input type="checkbox"/>

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